

**Silicon PNP transistor epitaxial type  
A5861**
**[ Applications ]**

LED TV

**[ Feature ]**

 Very low collector saturation voltage  $V_{CE(sat)} = -430\text{mV}$  (Max.) at  $I_C = -2\text{A}$ ,  $I_B = -0.1\text{A}$ 
**[ Absolute maximum ratings (Ta=25C) ]**

Characteristic	Symbol	Maximum ratings	Unit
Collector-base voltage	VCBO	-60	V
Collector-emitter voltage	VCES	-60	V
Collector-emitter voltage	VCEO	-50	V
Emitter-base voltage	VEBO	-6	V
Collector current (DC)	IC	-5	A
Collector current (Pulse)	IC	-7.5	A
Base current (DC)	IB	-1.2	A
Junction temperature	Tj	150	C
Storage temperature	Tstg	-55 to 150	C

**[ Electrical characteristics (Ta=25C) ]**

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BVCBO	-60	-	-	V	$I_C = -100\mu\text{A}$ , $I_E = 0\text{A}$
Collector-emitter breakdown voltage	BVCES	-60	-	-	V	$I_C = -100\mu\text{A}$
Collector-emitter breakdown voltage	BVCEO	-50	-	-	V	$I_C = -1\text{mA}$ , $I_B = 0\text{A}$
Emitter-base breakdown voltage	BVEBO	-6	-	-	V	$I_E = -10\mu\text{A}$ , $I_C = 0\text{A}$
Collector cut-off current	ICBO	-	-	-100	nA	$V_{CB} = -60\text{V}$ , $I_E = 0\text{A}$
Emitter cut-off current	IEBO	-	-	-100	nA	$V_{EB} = -6\text{V}$ , $I_C = 0\text{A}$
DC current gain	hFE	220	-	560	-	$V_{CE} = -2\text{V}$ , $I_C = -0.5\text{A}$
Collector-emitter saturation voltage 1	$V_{CE(sat)1}$	-	-	-195	mV	$I_C = -1\text{A}$ , $I_B = -50\text{mA}$
Collector-emitter saturation voltage 2	$V_{CE(sat)2}$	-	-	-430	mV	$I_C = -2\text{A}$ , $I_B = -0.1\text{A}$
Base-emitter saturation voltage	$V_{BE(sat)}$	-	-	-1.2	V	$I_C = -2\text{A}$ , $I_B = -0.1\text{A}$
Transition frequency	fT	-	150	-	MHz	$V_{CE} = -10\text{V}$ , $I_E = 0.5\text{A}$
Collector output capacitance	Cob	-	50	-	pF	$V_{CB} = -10\text{V}$ , $f = 1\text{MHz}$ , $I_E = 0\text{A}$
Turn on time	ton	-	30	-	ns	$V_{CC} = -25\text{V}$ , $I_C = -1\text{A}$
Storage time	tstg	-	230	-	ns	$-I_{B1} = I_{B2} = -0.1\text{A}$
Fall time	tf	-	15	-	ns	$V_{BE} = -5\text{V}$

Notice 1) These are measured data of transistors assembled by PHENITEC SEMICONDUCTOR Corp. and are for reference only.

Notice 2) The contents described herein are subject to change without notice.

Fig.1 IC - VBE(on)  
at VCE = -2V, Ta = 25C

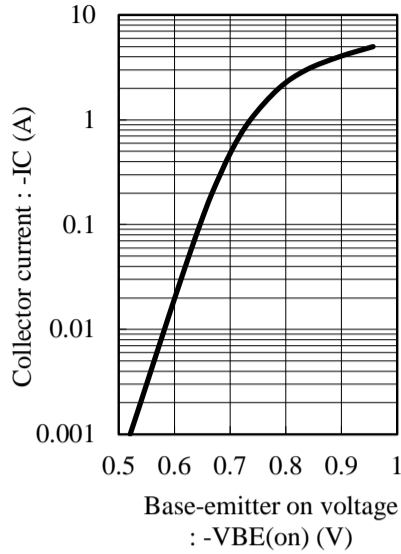


Fig.2 hFE - IC  
at VCE = -2V, Ta = 25C

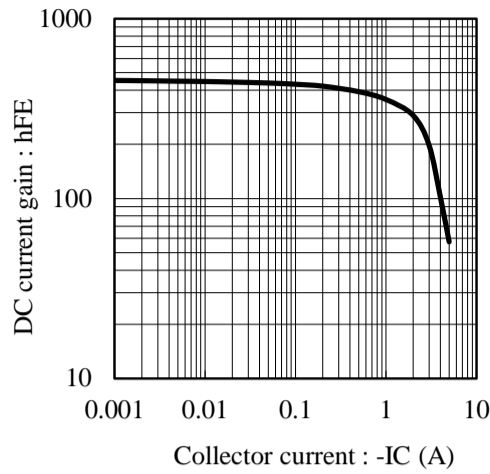


Fig.3 VCE(sat) - IC  
at IC/IB = 20, Ta = 25C

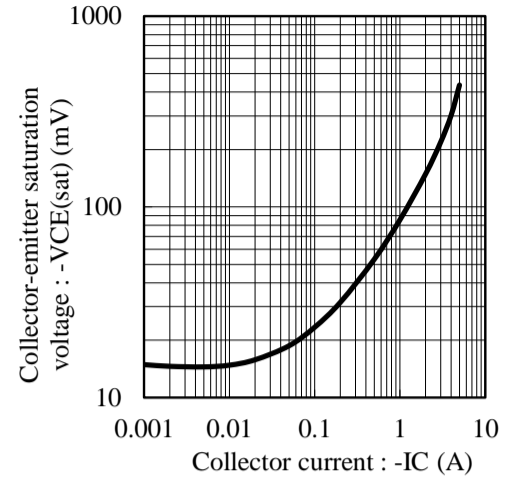


Fig.4 VCE(sat) - IC  
at IC/IB = 50, Ta = 25C

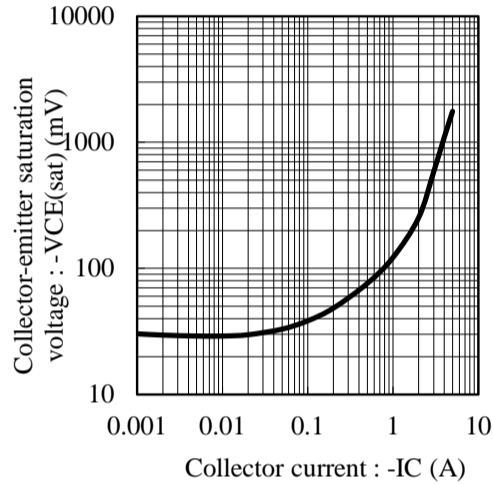


Fig.5 VBE(sat) - IC  
at IC/IB = 20, Ta = 25C

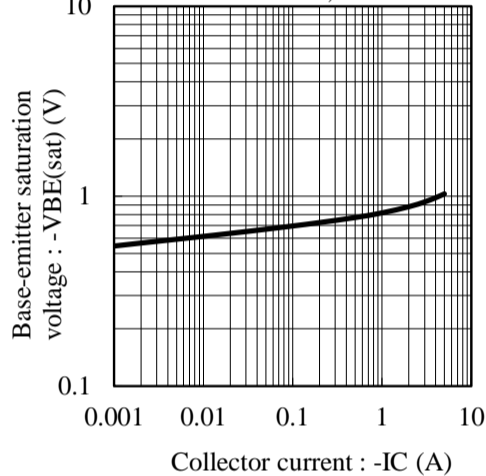


Fig.6 VBE(sat) - IC  
at IC/IB = 50, Ta = 25C

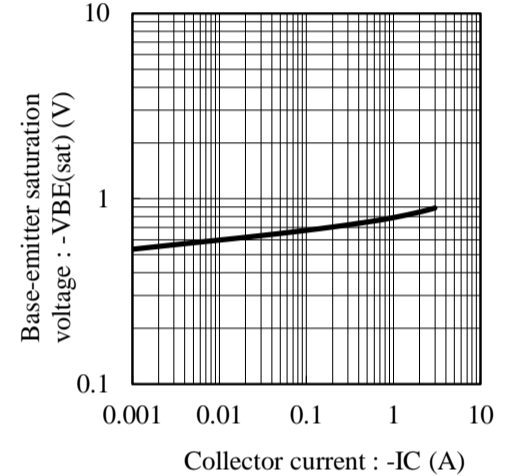


Fig.7 fT - IE  
at VCE = -10V, Ta = 25C

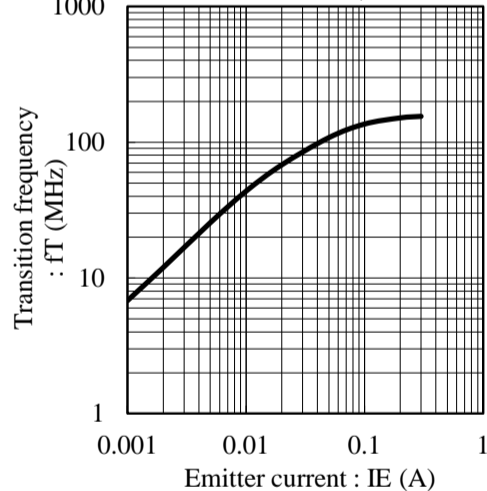


Fig.8 Cob - VCB  
at f = 1MHz, Ta = 25C

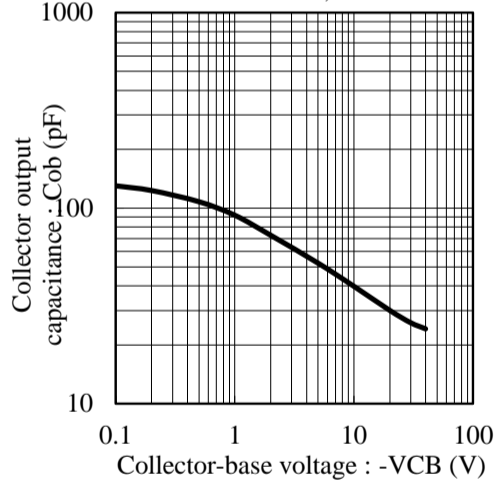


Fig.9 Cib - VEB  
at f = 1MHz, Ta = 25C

