

**Silicon PNP transistor epitaxial type  
AP941**

**[ Applications ]**

Switching and low frequency signal amplifier

**[ Feature ]**

MMBT2907A compatible with small chip size

High level collector current  $I_C = -600\text{mA}$

DC current gain certified at high collector current  $h_{FE} \geq 50$  at  $I_C = -500\text{mA}$

Low collector saturation voltage  $V_{CE(sat)} = -0.4\text{V(Max.)}$  at  $I_C = -150\text{mA}$ ,  $I_B = -15\text{mA}$

**[ Absolute maximum ratings (Ta=25C) ]**

Characteristic	Symbol	Maximum ratings	Unit
Collector-base voltage	VCBO	-60	V
Collector-emitter voltage	VCEO	-60	V
Emitter-base voltage	VEBO	-5	V
Collector current	IC	-600	mA
Junction temperature	Tj	150	C
Storage temperature	Tstg	-55 to 150	C

**[ Electrical characteristics (Ta=25C) ]**

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BVCBO	-60	-	-	V	$I_C = -10\mu\text{A}$
Collector-emitter breakdown voltage	BVCEO	-60	-	-	V	$I_C = -10\text{mA}$
Emitter-base breakdown voltage	BVEBO	-5	-	-	V	$I_E = -10\mu\text{A}$
Collector cut-off current	ICEX	-	-	-50	nA	$V_{CE} = -30\text{V}$ , $V_{EB} = -0.5\text{V}$
Collector cut-off current	ICBO	-	-	-10	nA	$V_{CB} = -50\text{V}$
Emitter cut-off current	IEBO	-	-	-50	nA	$V_{EB} = -4\text{V}$
DC current gain 1	$h_{FE} 1$	75	-	-	-	$V_{CE} = -10\text{V}$ , $I_C = -0.1\text{mA}$
DC current gain 2	$h_{FE} 2$	100	-	-	-	$V_{CE} = -10\text{V}$ , $I_C = -1\text{mA}$
DC current gain 3	$h_{FE} 3$	100	-	-	-	$V_{CE} = -10\text{V}$ , $I_C = -10\text{mA}$
DC current gain 4	$h_{FE} 4$	100	-	300	-	$V_{CE} = -10\text{V}$ , $I_C = -50\text{mA}$
DC current gain 5	$h_{FE} 5$	50	-	-	-	$V_{CE} = -10\text{V}$ , $I_C = -500\text{mA}^*$
Collector-emitter saturation voltage 1	$V_{CE(sat)} 1$	-	-	-0.4	V	$I_C = -150\text{mA}$ , $I_B = -15\text{mA}^*$
Collector-emitter saturation voltage 2	$V_{CE(sat)} 2$	-	-	-1.6	V	$I_C = -500\text{mA}$ , $I_B = -50\text{mA}^*$
Base-emitter saturation voltage 1	$V_{BE(sat)} 1$	-	-	-1.3	V	$I_C = -150\text{mA}$ , $I_B = -15\text{mA}^*$
Base-emitter saturation voltage 2	$V_{BE(sat)} 2$	-	-	-2.6	V	$I_C = -500\text{mA}$ , $I_B = -50\text{mA}^*$
Transition frequency	f T	200	-	-	MHz	$V_{CE} = -20\text{V}$ , $I_E = 50\text{mA}$
Collector output capacitance	Cob	-	-	8	pF	$V_{CB} = -10\text{V}$ , $f = 1\text{MHz}$ , $I_E = 0\text{A}$
Collector input capacitance	Cib	-	-	30	pF	$V_{EB} = -2\text{V}$ , $f = 1\text{MHz}$ , $I_C = 0\text{A}$

\*pulse test

Notice 1) These are measured data of transistors assembled by PHENITEC SEMICONDUCTOR Corp. and are for reference only.

Notice 2) The contents described herein are subject to change without notice.

Fig.1 hFE - IC  
at VCE= -10V, Ta= 25C

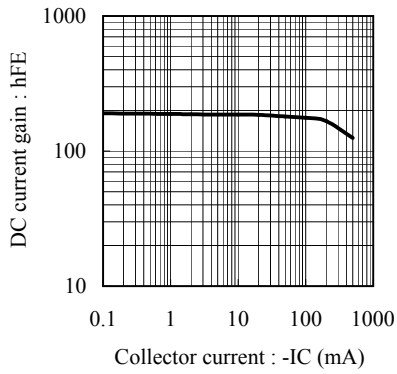


Fig.2 VCE(sat) - IC  
at IC/IB= 10, Ta= 25C

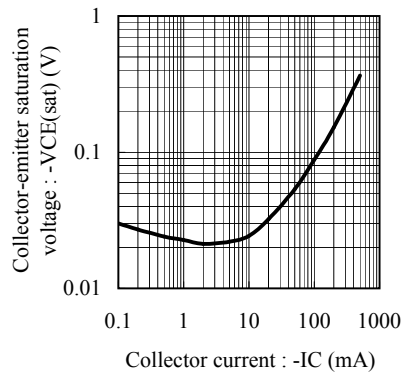


Fig.3 VBE(sat) - IC  
at IC/IB= 10, Ta= 25C

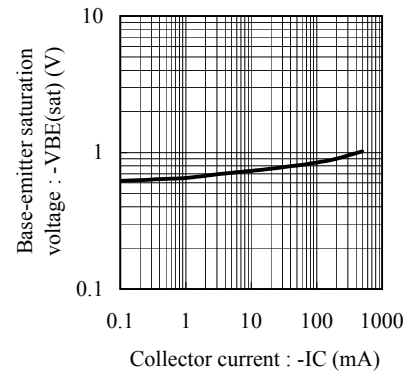


Fig.4 fT - IE  
at VCE= -20V, Ta= 25C

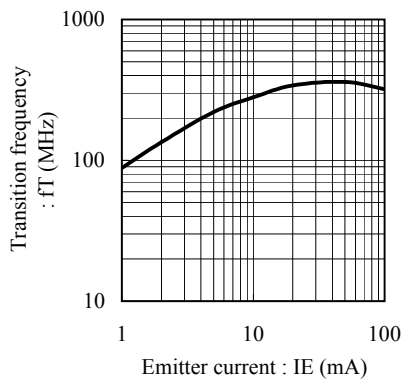


Fig.5 Cob - VCB  
at f= 1MHz, Ta= 25C

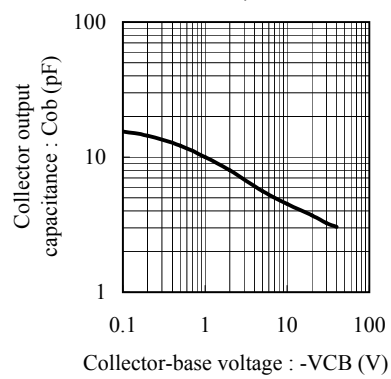


Fig.6 Cib - VEB  
at f= 1MHz, Ta= 25C

